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UTILITY PATENT APPLICATION TRANSMITTAL
(Only for new nonprovisional applications under 37 C F R § 1 53(b))

Attorney Docket No.	112025-0180
First Inventor or Application Identifier	Andrew McRae
Title	A METHOD FOR HIGH SPEED PACKET CLASSIFICATION
Express Mail Label No.	EL056799238US

APPLICATION ELEMENTS
See MPEP chapter 600 concerning utility application contents

ADDRESS TO: Assistant Commissioner for Patents
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Washington, DC 20231

jc530 U.S. PTO 09/557480 04/24/00

☒ *Fee Transmittal Form (e.g., PTO/SB/17)
(Submit an original and a duplicate for fee processing)

2. ☒ Specification [Total Pages 28]
(preferred arrangement set forth below)
- Descriptive title of the Invention
- Cross References to Related Applications
- Statement Regarding Fed sponsored R & D
- Reference to Microfiche Appendix
- Background of the Invention
- Brief Summary of the Invention
- Brief Description of the Drawings *(if filed)*
- Detailed Description
- Claim(s)
- Abstract of the Disclosure

3. ☒ Drawing(s) [Total Sheets 11]

4. Oath or Declaration [Total Pages 2]
a. ☒ Newly executed (original copy)
b. ☐ Copy from a prior application (37 C.F.R § 1.63(d))
(for continuation/divisional with Box 17 completed)
[Note Box 5 below]
i. ☐ DELETION OF INVENTOR(S)
Signed statement attached deleting inventor(s) named in the prior application, see 37 C F R §§ 1 63(d)(2) and 1 33(b).

5. ☐ Incorporation By Reference (*useable if Box 4b is checked*)
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered to be part of the disclosure of the accompanying application and is hereby incorporated by reference therein

6. ☐ Microfiche Computer Program (*Appendix*)

7. Nucleotide and/or Amino Acid Sequence Sequence Submission
((if applicable, all necessary)
a. ☐ Computer Readable Copy
b. ☐ Paper Copy (Identical to computer copy)
c. ☐ Statement verifying identity of above copies

ACCOMPANYING APPLICATION PARTS

8. ☒ Assignment Papers (cover sheet & document(s))
9. ☐ 37 C.F.R.§3.73(b) Statement (*when there is an assignee*) ☒ Power of Attorney
10. ☐ English Translation Document (*if applicable*)
11. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS Citations
12. ☐ Preliminary Amendment
13. ☒ Return Receipt Postcard (MPEP 503) (*Should be specifically itemized*)
14. ☐ *Small Entity Statement(s) (*(PTO/SB/09-12)*) ☐ Statement filed in prior application, Status still proper and desired
15. ☐ Certified Copy of Priority Document(s) (*if foreign priority is claimed*)
16 ☐ Other:

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17. If a CONTINUING APPLICATION, *check appropriate box and supply the requisite information below and in a preliminary amendment:*
☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: /
Prior application Information: Examiner *Group/Art Unit:*

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re The Application of:
Andrew McRae

Serial No.: Not Yet Assigned

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For: A METHOD FOR HIGH SPEED PACKET CLASSIFICATION

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April 24, 2000

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_X_ Check for $40
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UNITED STATES PATENT APPLICATION

of

Andrew McRae

for a

A METHOD FOR HIGH SPEED PACKET CLASSIFICATION

A METHOD FOR HIGH SPEED PACKET CLASSIFICATION

BACKGROUND OF THE INVENTION

Field of the Invention

The invention relates generally to the classification and/or filtering of data packets, and in particular, to the high speed filtering and/or classification of data packets.

Background Information

Various systems use access control lists (ACLs) to provide security, filtering and so forth. In the context of a networking system, besides security control, an ACL provides for packet classification that could be used in a range of applications such as quality of service parameters, queuing classes or other actions. In fact, an ACL could be viewed as a set of rules, with each rule having an associated value, class or action. For instance, the ACL, once matched, may indicate to a router what type of action should be performed on the matched packet. In its simplest form, the action may be to allow the matched packet to proceed towards its destination (i.e., "permit"). Conversely, if there is no match, the action may be to drop the packet (i.e., "deny"). In a more sophisticated form, complex policies and filtering rules may be implemented in the ACL to determine the course of the data packet.

There are instances where ACLs are sequential in nature, in which an incoming or an outgoing packet is sequentially compared against a list of rules. A system could have thousands of access rules and it is not uncommon to have hundreds of rules in an ACL. There are many instances in which the rules are quite complex, providing a capability of checking source and destination addresses, protocol selection (such as Transmission Control Protocol (TCP), User Datagram Protocol (UDP) and etc.), layer 4 (L4) port num-

bering and so forth. Stated differently, the more complex a rule becomes, the longer it takes for a processor to process that rule. Because a processor processes literally by comparing each rule sequentially with a packet until a match is found, the classification performance is highly dependent on the complexity of the rules and is processor intensive. Another problem is the non-determinism of the match in which the processing time is dependent on which rule the packet matches (i.e., the earlier the match is found, the shorter the processing time).

One method of obtaining a faster access is to predetermine the frequency of access of the various rules and to place the most selected ones at the top of the list. However, this method is highly dependent on the packet mix of the networking system and is not readily scalable. A delay in its worst scenario is when no rule matches, in which case, the processor performs a default rule. In a permit/deny action, the default rule is usually a "final deny rule." Additionally, an implicit attribute of a sequentially searched access list is the rule that is matched is the "first matching rule" and is not necessarily the "best matching rule" which may occur further down the list.

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SUMMARY OF THE INVENTION

There is a need for matching a packet with an ACL in a manner that is faster than a sequential search, uses reasonable memory space, and may be performed with determinism (i.e., predictable interval) regardless of the size of the ACL or complexity of the access rules and provides for a choice of matching rules.

The present invention comprises a technique for processing classification and/or security filtering rules using bitmaps as representations. In one example, a packet header involved in the packet classification is divided into sections (fields) such as 16 bit portions. Thereafter, a first-level data lookup table is built for each of the fields by iterating from 0 to a maximum value of the packet header field being processed. For each iteration, a bitmap is created representing which filter rules match this particular value. Each bit position of the bitmap represents a rule in the ACL. Each created bitmap is compared with the previously created bitmaps for this field, if any, to determine if there is a matching bitmap. If there is a match, the equivalence set index of the matching bitmap is as-

signed to the packet header field value currently being processed. An equivalent set is a set of unique values that represents unique bitmaps for a particular field.

If there are no matching bitmaps, a new entry is created which has a new equivalence set index value and the created bitmap is assigned to it. Thus, the first level lookup tables are formed with the equivalence set indices and their corresponding bitmaps. Next, the first lookup tables are "cross-producted" to form intermediate lookup tables, if any. The cross-producting creates yet another equivalence set indices and related bitmaps. The intermediate lookup tables are further cross-producted until eventually a single table is left, which is the final lookup table. The final lookup table comprises the final bitmaps that provides for all theoretically possible combinations of outcomes given in any packet header values. Stated differently, for any possible outcome, there is a bitmap indicating which rules are matching.

The bitmap can be used to selectively provide a desired result of the classification. For instance, a first matching rule is represented by the first bit set in the bitmap. The best matching rule is then determined by processing the bitmap and selecting the most appropriate rule; a complete set of matching rules is represented by the assertion of all bits in the bitmap.

Other advantages and features will be apparent in the detailed description to follow.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention description below refers to the accompanying drawings, of which:

Fig. 1 depicts a conventional network in which an embodiment of the invention may be implemented;

Fig. 2 shows an example of a network device with an embodiment of the invention implemented;

Fig. 3 shows a field as an interval in a one-dimensional scale along with rules expressed as a subset of the interval;

Fig. 4 shows how a second field can be added to the one dimensional scale in Fig. 3 to form a two-dimensional scale;

Fig. 5 shows an exemplary flow diagram that depicts the creation of data lookup tables for data packets;

Fig. 6 depicts the splitting of the data network packet headers into portions to form data lookup tables;

Fig. 7 shows an exemplary flow diagram that depicts the creation of the first data lookup tables;

Fig. 8 shows an exemplary flow diagram that depicts the creation of bitmaps for a packet header field value;

Fig. 9 illustrates two data lookup tables being merged via cross-producting;

Fig.10 shows an exemplary flow diagram that depicts the creation of the subsequent levels of data lookup table;

Fig.11 illustrates equivalence sets of lookup tables being cross-producted to form a new equivalence set for a next level table; and

Figure 12 illustrates a lookup table hierarchy suitable for implementation in hardware.

DETAILED DESCRIPTION OF AN ILLUSTRATIVE EMBODIMENT

In accordance with the invention, representations of packet classification rules are created as bitmaps which are indexed by lookup tables for packet classifications.

The invention may be implemented in a network 100, as shown in **Figure 1**. The network may include a network cloud 110 which may be a combination of transmission lines, backbones, switches, routers and repeaters, dependent on the extent of the network, and allows various devices connected to the network to communicate with each other. These various devices may include simple computers such as personal computers (PCs) and workstations 112, that transmit or receive data, or they may be sophisticated resource

servers 114 that store various resources. For instance, the Internet is a giant web that connects large numbers of these simple computers and resource servers together for the interchange of information. Information flows in packets wherein an attached header includes source and destination addresses, among others, that inform the devices encountered in the web as to how the packets are to be forwarded.

For various reasons such as quality of service or security, access control lists (ACLs) may be used by various devices in the web, including the computers and the resource servers themselves. **Figure 2** illustrates a network device 200 such as a router with an ACL that is used for classification of receiving packets, for example. In particular, the ACL is composed of a plurality of data lookup tables 240 constructed in accordance with the invention soon to be described. The router comprises a processing unit 212 and a memory unit 214 coupled together by a bus 216. Further coupled to the bus may be a plurality of input/output (I/O) interfaces 218 that interact with other routers and devices within the network. In one example, an operating system (OS) 230 resides in the memory unit 214 along with the data lookup tables 240. Together, they facilitate in the classification of the receiving packets when executed by the processing unit 212. In one instance, the classification of the packets pertains to the type of quality of service a packet is to receive.

The memory unit 214 in which the data lookup tables 240 reside may be a volatile memory such as a Dynamic Random Access Memory (DRAM). The data lookup tables 240 may also reside in a non-volatile memory such as a Read Only Memory (ROM) or a Flash memory. Further, the data lookup tables may be stored in a storage medium such as a magnetic or an optical disk. Collectively, the mentioned memories, storage mediums and the like will be referred to as a processor executable medium. Additionally, the data lookup tables may be configured in hardware such as a content addressable memory (CAM) or an application specific integrated circuit (ASIC).

An action of matching a packet against a database of rules (or rulesets) can be implemented as a memory lookup. For instance, if a packet header has a total of 114 bits (each Internet Protocol (IP) address: 32 bits, each layer 4 (L4) port number: 16 bits, 8 bit protocol, 8 bit type of service (TOS)/Precedence, flags: 2bits), the 114 bits could be used

as an entry to a memory bank such as a CAM to classify the incoming packets. While this guarantees that every incoming packet would be classified, the memory size involved would make the implementation impractical.

An observation is made that the rules themselves need not treat the packet header as one single header, but instead, each packet header field could be treated separately. For example, each rule can separately specify values for IP source and destination addresses, IP protocol and etc. Some fields may have implicit dependence, such as when L4 port numbers are specified, the IP protocol may need to be specified as either a Transmission Control Protocol (TCP) or a User Datagram Protocol (UDP). In instances where each rule makes no reference to a particular field, such as a 32 bit IP source address, an assumption can be made that all values of this field will match (i.e., a wildcard). A wildcard IP address can be simply treated as a value of zeros and a mask of zeros.

One way of viewing a field is to consider it as an interval in a one-dimensional scale with values from 0 to 2^n where n is the number of bits in the field as shown in **Figure 3**. Each rule to be considered can be expressed as a ruleset of the above interval, either as a point (a single value with a 255.255.255.255 mask), an interval (a value with a contiguous mask, e.g., 192.55.99.0/255.255.255.0) or as a set of intervals (such as a non-contiguous mask, e.g., 192.55.0.1/255.255.0.255). When a packet is to be matched against a ruleset, a point on the interval representing a packet value is used to create an intersecting line. A rule matches this packet value if the line intersects the interval of the rule. The first rule that the line intersects is the first matching rule. Thus, the process of determining which rules match is expressed in terms of determining which intervals (i.e., rules) belong to any particular point on the one-dimensional scale.

Another observation is that in an access control list (ACL), each packet field is disjoint from the others (with certain exceptions). For example, the source IP address and the destination IP address can be specified separately and matched separately because they have no intrinsic relationship in the packet matching process. This property can be used to expand the above model of matching points in a set of intervals by considering other fields as separate dimensions. **Figure 4** shows how a second field (e.g., IP destination address) can be added to the existing IP source address dimension. Each rule has a

value/mask for both the IP source and destination addresses, and is represented as an interval. When plotted on a two-dimensional scale, intervals for each field which matches a rule sweep out an area that represents a portion of space where the rule matches both fields. For instance, rules that have a 255.255.255.255 mask in both fields are represented by a point, and rules in which one field has a 255.255.255.255 mask appear as a line. Accordingly, the region that each rule covers indicates the area in which the packets can match.

The process of expanding the dimensions of the ruleset space can further continue to accommodate other fields required in packet matching (e.g., IP source & destination, TOS and precedence, protocol, L4 source and destination port). Once a set of regions is discovered, the next step is to select the appropriate region from the matching set. Thus, matching a packet is a case of discovering in which regions a single point lies in an N-dimensional space given that the rulesets are defining the regions in each dimension, and the packet field values determine the location of the single point in that space.

So far there are considerable references that describe this. For further reading, a paper of interest is Pankaj Gupta and Nick McKeown, "Packet Classification on Multiple Fields," Proc. Sigcomm, September 1999, Harvard University. However, the method described generally suffers from either a large memory requirement or from excessive processing. In general, it is very difficult to create a viable algorithm that takes into consideration the complete possible combinations of the packet header fields.

Another observation is made that the pattern of data in both the rules and the packets being matched are usually limited to a small subset of possible data range. For example, TCP port numbers have a range from 0 to 65535. However, in reality, only a small number of those port numbers are of any real interest. This allows for a large degree of compression to take place.

As a comparison, the sequential searching of the ACL is analogous to matching the packet by moving through the different regions of the rulesets and checking whether the packet lies within all regions of the rule. This is in effect saying "given a set of regions representing the rule, check each one to see if this packet lies within the region."

In accordance with the invention, a more efficient approach would be to say "given a packet, determine the set of regions that this packet lies and then select the best (or first) matching rule. In other words, instead of matching the rules to the packet, the packet values are used to deterministically arrive at the matching rules.

With this concept in mind, **Figure 5** shows an exemplary procedure that allows for the creation of data tables for header values that match against a set of classification rules. Briefly, in block 502, the packet header involved in the packet classification is divided into sections (fields) such as 16 bit portions. Once, this is performed, a data lookup table is built for each of the packet header fields. In particular, a bitmap is created for each header field value to represent the matching filter rules. In block 504, the created data lookup tables, typically, one for each packet header field, is merged two at a time to form intermediate second level data lookup tables, if any. Thus, if there are 8 initial data lookup tables, they are merged two at a time to produce 4 second level data lookup tables. The second level data lookup tables are then merged two at a time to form intermediate third level lookup tables. The merging proceeds until one final data lookup table is formed. Note that the procedure is not limited to merging of two tables at a time, but the technique may be had to merge three or more tables at a time until a final table is formed. In block 506, the final table is formed and the classification results are assigned to the table. The results in the final data lookup table represent all the possible packets to be classified. Thus, each packet header entry has a bitmap representing the filtering rules that matches this entry. The bitmap can be used to selectively provide a desired result of the classification. For instance, a first matching rule is represented by the first bit set in the bitmap; the best matching rule is determined by processing the bitmap and selecting the most appropriate rule; and a complete set of rules that match is represented by the full bits set in the bitmap.

The run time operation, on average, is faster than the sequential access list, and further, is deterministic (i.e. no matter how many entries there are in the ACL, the same number of processor cycles are consumed). The passages to follow will now describe in detail the mechanics of how the data tables are created.

As previously mentioned, the packet header can be split into fields and specified separately, such as IP source and destination addresses, protocol, L4 port numbers etc. This is shown in **Figure 6**. While there are many ways of splitting the packet header, it is desirable to split the header into consistent bit sizes. Preferably, the bit size is 16 bits, one reason being that the port numbers are 16 bits, and port numbers are dealt with differently to that of the IP addresses. Specifically, port numbers do not have a value/mask arrangement, but have numeric ranges; it is much more convenient to deal with them as a single value rather than splitting them up.

In the example where each block is 16 bits, some smaller header fields are grouped together to form a 16 bit block and the TCP flags section only uses 2 bits. For protocols such as Internet Control Message Protocol (ICMP), the source port block can be reused as the ICMP type fields. Correspondingly, each rule in the ACL is split into 8 blocks, with each block associated with a field block.

Given a sample set of access lists below:

```
access-list 101 deny tcp 192.100.1.0 255.255.255.0 eq smtp
access-list 101 permit ip 192.100.1.0 255.255.255.0
access-list 101 permit ip 192.101.1.0 255.255.255.0
access-list 101 deny tcp any any eq 21
access-list 101 deny udp any any eq 80
```

Taking one of these blocks as an example, such as the first 16 bits of the IP source address, the access list entries often contain repeated or wildcard entries, thus the following ruleset can be formed :

Table 1

Rule Number	Value	Mask
1	192.100	255.255
2	192.100	255.255
3	192.101	255.255
4	0.0	0.0

5	0.0	0.0

The rule is processed by creating a data structure termed an "equivalence set." Basically, an equivalence set is a set of unique values that exist across all rules for a particular field. For each entry in the equivalence set, an indication is kept of which rules this entry represents, the rationale being that a value may appear in more than one rule. For example, 192.100/255.255 value above appears in both rule 1 and rule 2. By using a bitmap, rules associated with each equivalence set may be tracked. Each equivalence set entry is assigned an identifying index. So for the example above, the following equivalence set is created as shown in table 2:

Table 2

Equivalence set Index	Value/mask	ACL entries (bitmap) 1 2 3 4 5
0	192.100/255.255	1 1 0 0 0
1	192.101/255.255	0 0 1 0 0
2	0.0/0.0	0 0 0 1 1

By comparing table 1 with table 2, a compression has taken place in that out of the 5 rules, within this field there are only 3 possible outcomes. Thus, after determining how many unique intervals (rules) there are in the scale from 0 to 2^{16}-1, the preliminary equivalence set reduces the original rules down to a minimal data set. This concept is used to build first level lookup tables that map the 16 bit packet values to a smaller index value.

As shown in **Figure 7**, in step 702, the first level lookup table is built by iterating from 0 to a maximum value of the packet header field being processed, which in this instance is 65536 possible values. The following steps occur for each iteration. In step 704, a bitmap is created representing which filter rules match this particular packet header field value. Further representation of the filter rules by the bitmap will be de-

scribed with respect to **Figure 8**. In step 706, the created bitmap is compared with other previously created bitmaps for this field, if any, to determine if there is a matching bitmap. If a matching bitmap is found, in step 708, the equivalence set index of the matching bitmap is assigned to the packet header field value currently being processed. Otherwise, if there are no matching bitmaps, in step 710, a new entry is created which has a new equivalence set index value and has the created bitmap assigned to it, shown in step 712. In step 714, the packet field value is incremented and the process is repeated until the last iteration value has been reached. In step 716, the equivalence set index values and their corresponding bitmaps are used to form the first level lookup table.

Figure 8 represents a flow diagram that shows the creation of a bitmap for a packet header field value. In step 802, an empty bitmap is created in which the number of bits represent the number of filtering rules applicable to the packet header. In step 804, for a particular field value, the filter rules are scanned from the first to the last to determine which rules match. In step 806, when a value matches the rule being tested, the bit that corresponds to that rule is set. Alternatively, if the value does not match the rule, then the bit remains clear to indicate that the rule does not match, shown in step 808. In steps 810-812, the next rule is tested against the packet header field value until all the rules have been scanned. In step 814, the completed bitmap now represents the filter rules that match the corresponding packet header field value. This process is repeated for each iterated field value.

Note that for field values such as an IP address, the value is considered to match an entry if that packet header value is covered by that entry's value/mask. For port numbers, instead of value/mask, a numeric range is used.

An example of the new equivalence set is shown below:

Table 4

First level lookup Table index values	Equivalence set Index	ACL entries (bitmap) 1 2 3 4 5
0-48995	0	0 0 0 1 1

48996 (192.100)	1	1 1 0 1 1
48997 (192.101)	2	0 0 1 1 1
48998-65535	0	0 0 0 1 1

For each possible value of the 16 bit field of the packet header being processed, there is an equivalence set index, and the equivalence set entry associated with this index contains a bitmap of which ACL rules match this value. Note that the size of the original equivalence set being scanned for matching entries is generally small, and the bitmap processing optimizes the creation of new entries. Hashing of the bitmap may be used in ACLs that have a large number of rules (and thus a long bitmap) or is too complex to be reduced to bitmaps as not to cause excessive processing. They may also be stored and addressed separately. Note further that the new equivalence set has no relationship to the original equivalence set. A variation of this process could directly scan the rules and set the bitmap bit accordingly, without the creation of a preliminary equivalence set. Which is more efficient depends entirely on the complexity and size of the rule set.

An equivalence set is built for each of the 8 fields and results in 8 first level lookup tables, with all the entries in the lookup tables referring to the indices of the 8 equivalence sets associated with each packet field.

The next step combines these 8 separate equivalence sets through a technique known as "cross-producting" to a set of 4 intermediate lookup tables and equivalence sets. These are further reduced so that eventually only a single table is left, which is the final lookup table.

The cross-producting operates by creating a yet another equivalence set, iterating through the two sets being combined and creating a cross-product of all the possible outcomes.

Figure 9 shows a creation of a next level data lookup table by cross-producting two lookup data tables to be merged and is further described with respect to the flow diagram in **Figure 10**. In step 1002, a new data table (table Z) is created, the size of the ta-

ble being a product of the sizes of the two tables being merged. The index values are iterated for one of the tables (table X) to be merged. In step 1004, for each iterated value of table X, the index values of the other table (table Y) are also iterated. In step 1006, for each iterated index value of table Y, an AND operation is performed on the bitmaps associated with the indices of table X and Table Y to form a new bitmap to be allocated to table Z. In step 1008, the newly created bitmap is compared with the previously created bitmaps, if any, allocated to table Z to determine if there is a match. If there is a match, then in step 1010, the created bitmap is assigned the equivalence set index value of the matched bitmap. Otherwise, in step 1012, a new equivalent set index value is assigned to the created bitmap. The bitmap is then allocated to Table Z. In steps 1014-1016, the process is repeated for the iterations of the index values of table Y constituting the inner loop and the iterations of the index values of table X constituting the outer loop. Once all the iterations are complete, in step 1018, the lookup data table Z represents the merged result of the two tables (table X and Table Y). Note that this is in effect finding the intersection of the two entries, and the bits are set in the bitmap only when both contributing entries have the bits set. This discovers the set of ACL rules that match for the particular two values of the contributing equivalence set.

In essence, what the crossproducting is doing is performing a intersection on the regions covered by the two equivalence sets, and creating a new equivalence set representing all the possible outcomes of the combination of any values in the two packet fields. As an example, refer to **Figure 11** to consider a packet with the IP source address of 192.101.1.20 which is used as input during runtime lookup. Using the two 16 bit blocks as indices into the two first level tables for each of these fields (i.e., upper 16 bits of IP source and lower 16 bits of IP source address), the upper 16 bits would return an equivalence set index of 2, and the lower 16 bit lookup would return a value of 1. To use these results to look up the next level, the two results are used as index values into the next level's lookup table, obtaining a result of 2. The bitmap associated with this equivalence set entry shows that the rules matching this packet are rules 3, 4, and 5.

As can be seen, though the theoretical maximum size of the new equivalence set is 6, the actual number of possible outcomes is lower (3 in this case) because at each level

there is commonality across the rules. If every rule was different from every other in every field, there is no compression, but the nature of the rules (describing real filters for real packets) usually allows high levels of compression to be achieved.

This final equivalence set provides all the theoretical possible combinations of rules given any packet header values, and for any of these possible outcomes, there is a bitmap indicating which rules are matching. By doing a find-first-set on the bitmap, the first matching rule can be obtained. Because the bitmaps provide a list of all the rules that match a packet, it is possible to employ this in other ways, such as choosing the best match, or using the list as a set of signatures to check for in intrusion detection. The final lookup can also contain the ACL entry index or a pointer to the actual matching ACL entry to avoid the overhead of searching for the first bit set.

Once the first and intermediate data lookup tables are built, the bitmaps are no longer necessary and only the equivalence set indices are maintained. The first level lookup tables also include the field values that point to their respective indices. Note that full bitmaps need not be retained in the final lookup table, but the bitmaps can be customized according to the requirements of the classifying device. For instance, if the first matching rule is the only relevant classification, the bitmaps need not contain the full matching rule set but only the first matching rule. Thus, the bitmaps can be customized according to the classification rule.

At packet classification time, a packet field value is sectioned into fields and entered into their respective first level lookup tables, which generate the corresponding equivalent indices for those fields. Those indices enter their corresponding intermediate second level tables, which generate equivalent second level indices that are entered into the next intermediate tables, if any. This repeats until the final lookup table is indexed, which generates the corresponding bitmap representing matching rules for the packet field value. Thus, if 8 fields are employed, a total of 15 lookups will return the matching ACL entry, regardless of the number of rules in the ACL if the classification is done in software and performed sequentially. It is straightforward, however, to build the lookup tables in hardware such as a Flash memory, a CAM or an ASIC. In this situation, the 8 first level lookups can be done in parallel, and the results fed directly to the second level

lookups and etc. In other words, the various levels of the lookup tables are arranged in a hierarchy wherein the output of the lookup tables in one level are forwarded to the corresponding lookup tables in the next level and so forth as shown in **Figure 12**. Thus, a packet can be classified in a time of only 4 memory lookups expediting the classification process.

Representing packet classification rules as bitmaps which are indexed by lookup tables for packet classifications has been described. It will however be apparent that other variations and modifications may be made to the described embodiments, with attainment of some or all of their advantages. Therefore, it is the object of the appended claims to cover all such variations and modifications within the true spirit and scope of the invention.

What is claimed is:

CLAIMS

1. A method of performing data classification wherein data having a data header that is classified according to applicable rules, the method comprising the steps of:

a) creating an empty bitmap including a number of bits equal to a number of applicable rules;

b) applying the applicable rules to the data header and for each rule that matches the data header,

c) setting a bit corresponding to the rule in the bitmap; and

d) storing the bitmap in an entry that corresponds to the data header in a lookup table.

2. The method as in claim 1, further comprising the steps of:

e) assigning an index number to the bitmap; and

f) storing the index number as an index to the entry that contains the bitmap corresponding to the data header, wherein the index number is associated with the data header.

3. The method as in claim 2, further comprising the steps of:

iterating the data header from zero to a maximum value;

and for each iterated value of the data header,

performing the steps of a) – f).

4. The method as in claim 3, further comprising the steps of:

for each bitmap corresponding to the iterated value of the data header;

determining if there is a matching bitmap previously created;

assigning a new index number as an index to a new entry that contains the bitmap in the lookup table if there is no previous bitmap that matches; and

assigning the index number of the previous bitmap to the bitmap if the bitmap matches the previous bitmap.

5. A method of performing data classification wherein data having a data header that is classified according to applicable rules, the method comprising the steps of:

a) dividing the data header into a plurality of fields; and for each field,

b) creating an empty bitmap including a number of bits equal to a number of applicable rules;

c) applying the applicable rules to the field and for each rule that matches the field,

d) setting a bit corresponding to the rule in the bitmap; and

e) storing the bitmap as an entry that corresponds to the field in a lookup table associated with the field.

6. The method as in claim 5, further comprising the steps of:

f) assigning an index number to the bitmap; and

g) storing the index number as an index to the entry that contains the bitmap corresponding to the data header, wherein the index number is associated with the data header.

7. The method as in claim 6, further comprising the steps of:

iterating each field from zero to a maximum value;

and for each iterated value of the field,

performing the steps of b) – g).

8. The method as in claim 7, further comprising the steps of:

for each bitmap corresponding to the iterated value of the field;

determining if there is a matching bitmap previously created;

assigning a new index number as an index to a new entry that contains the bitmap in the lookup table if no previous bitmap matches; and

assigning the index number of the previous bitmap to the bitmap if the bitmap matches the previous bitmap.

9. The method as in claim 8, further comprising the steps of:

cross-producting a plurality of lookup tables corresponding to a plurality of fields of the data header to form a next level lookup table.

10. The method as in claim 9, wherein the step of cross-producting includes:

creating a next level bitmap from the cross-product of the plurality of bitmaps in the respective lookup tables;

determining if there is a matching next level bitmap previously created from the cross-product;

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assigning a new next level index number as an index to a new entry that contains the next level bitmap in the next level lookup table if no previous next level bitmap matches; and

assigning the next level index number of the previous next level bitmap to the next level bitmap if the next level bitmap matches the previous next level bitmap.

11. The method as in claim 10, further comprising the steps of:

for N number of lookup tables, wherein N is an integer,

cross-producting the N number of lookup tables; and

repeating the cross-producting on resulting lookup tables until one final lookup table remains.

12. The method as in claim 11, wherein the final lookup table contains bitmaps, each bitmap representing a complete set of rules that match a data header value, the method further comprising the step of using the bitmap to find the first matching rule of the header value.

13. The method as in claim 11, wherein the final lookup table contains bitmaps, each bitmap representing a complete set of rules that match a data header value, the method further comprising the step of using the bitmap to find the best matching rule of the header value.

14. The method as in claim 11, wherein the final lookup table contains bitmaps, each bitmap representing a complete set of rules that match a data header value, the method further comprising the step of using the bitmap to find all the rules applicable to the header value.

15. A processor executable medium having instructions contained therein, which when executed by a processor causes the processor to perform a method of performing data classification wherein data having a data header that is classified according to applicable rules, the method comprising the steps of:

a) creating an empty bitmap including a number of bits equal to a number of applicable rules;

b) applying the applicable rules to the data header and for each rule that matches the data header,

c) setting a bit corresponding to the rule in the bitmap; and

h) storing the bitmap in an entry that corresponds to the data header in a lookup table.

16. The processor executable medium as in claim 15, further comprising the steps of:

e) assigning an index number to the bitmap; and

f) storing the index number as an index to the entry that contains the bitmap corresponding to the data header, wherein the index number is associated with the data header.

17. The processor executable medium as in claim 16, further comprising the steps of:
iterating the data header from zero to a maximum value;
and for each iterated value of the data header,
performing the steps of a) – f).

18. The processor executable medium as in claim 17, further comprising the steps of:
for each bitmap corresponding to the iterated value of the data header;
determining if there is a matching bitmap previously created;
assigning a new index number as an index to a new entry that contains the bitmap in the lookup table if there is no previous bitmap that matches; and
assigning the index number of the previous bitmap to the bitmap if the bitmap matches the previous bitmap.

19. A processor executable medium of performing data classification wherein data having a data header that is classified according to applicable rules, the method comprising the steps of:
a) dividing the data header into a plurality of fields; and for each field,
b) creating an empty bitmap including a number of bits equal to a number of applicable rules;
c) applying the applicable rules to the field and for each rule that matches the field,
d) setting a bit corresponding to the rule in the bitmap; and

i) storing the bitmap as an entry that corresponds to the field in a lookup table associated with the field.

20. The processor executable medium as in claim 19, further comprising the steps of:

j) assigning an index number to the bitmap; and

k) storing the index number as an index to the entry that contains the bitmap corresponding to the data header, wherein the index number is associated with the data header.

21. The processor executable medium as in claim 20, further comprising the steps of:

iterating each field from zero to a maximum value;

and for each iterated value of the field,

performing the steps of b) – g).

22. The processor executable medium as in claim 21, further comprising the steps of:

for each bitmap corresponding to the iterated value of the field;

determining if there is a matching bitmap previously created;

assigning a new index number as an index to a new entry that contains the bitmap in the lookup table if no previous bitmap matches; and

assigning the index number of the previous bitmap to the bitmap if the bitmap matches the previous bitmap.

23. The processor executable medium as in claim 22, further comprising the steps of:

cross-producting a plurality of lookup tables corresponding to a plurality of fields of the data header to form a next level lookup table.

24. The processor executable medium as in claim 23, wherein the step of cross-producting includes:

creating a next level bitmap from the cross-product of the plurality of bitmaps in the respective lookup tables;

determining if there is a matching next level bitmap previously created from the cross-product;

assigning a new next level index number as an index to a new entry that contains the next level bitmap in the next level lookup table if no previous next level bitmap matches; and

assigning the next level index number of the previous next level bitmap to the next level bitmap if the next level bitmap matches the previous next level bitmap.

25. The processor executable medium as in claim 24, further comprising the step of:

for N number of lookup tables, wherein N is an integer,

cross-producting the N number of lookup tables; and

repeating the cross-producting on resulting lookup tables until one final lookup table remains.

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26. The process executable medium as in claim 25, wherein the final lookup table contains bitmaps, each bitmap representing a complete set of rules that match a data header value, the method further comprising the step of using the bitmap to find the first matching rule of the header value.

27. The process executable medium as in claim 25, wherein the final lookup table contains bitmaps, each bitmap representing a complete set of rules that match a data header value, the method further comprising the step of using the bitmap to find the best matching rule of the header value.

28. The process executable medium as in claim 25, wherein the final lookup table contains bitmaps, each bitmap representing a complete set of rules that match a data header value, the method further comprising the step of using the bitmap to find all the rules applicable to the header value.

29. An apparatus for performing data classification wherein data having a data header that is classified according to applicable rules, the apparatus comprising:

a memory having a lookup table, the lookup table having a plurality of indices associated with a plurality of bitmaps, each bitmap having a number of bits that indicates one or more matching rules; and

a circuit to receive the data header and use the data header as an entry to the lookup table to associate the data header with a bitmap in the lookup table.

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30. The apparatus as in claim 29, wherein the memory is a flash memory.

31. The apparatus as in claim 29, wherein the memory is a content addressable memory (CAM).

32. The apparatus as in claim 29, wherein the circuit is a processor.

33. An apparatus for performing data classification wherein data having a data header that is classified according to applicable rules, the apparatus comprising:

a circuit to divide the data header into a plurality of fields;

at least two first level lookup tables stored in a memory to receive the fields of the data header, the first level lookup tables configured to generate first outputs responsive to the received fields; and

a final level lookup table stored in the memory to receive the first outputs from the first level lookup tables, the first outputs indexing a bitmap in the final level lookup table, the bitmap representing matching rules of the data header.

34. The apparatus as in claim 33, further comprising:

at least two intermediate level lookup tables stored in the memory to receive the first outputs from the first level lookup tables, the intermediate level lookup tables configured to generate intermediate outputs responsive to the received first outputs; and

the final level lookup table to receive the intermediate outputs from the intermediate level lookup tables, the intermediate outputs indexing the bitmap in the final level lookup table.

35. The apparatus as in claim 34, wherein the memory is a flash memory.

36. The apparatus as in claim 34, wherein the memory is a content addressable memory (CAM).

37. The apparatus as in claim 34, wherein the circuit is a processor.

38. An apparatus for performing data classification wherein data having a data header that is classified according to applicable rules, the apparatus comprising:

means for lookup having a plurality of indices associated with a plurality of bitmaps, each bitmap having a number of bits that indicates one or more matching rules; and

means for receiving and using the data header as an entry to the lookup means to associate the data header with a bitmap in the lookup table.

39. An apparatus for performing data classification wherein the data having a data header that is classified according to applicable rules, the apparatus comprising:

means for dividing the data header into a plurality of fields;

means for receiving the fields of the data header, field receiving means generating first outputs responsive to the received fields; and

means for receiving the first outputs from the field receiving means, the first outputs indexing a bitmap in the first outputs receiving means, the bitmap representing matching rules of the data header.

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ABSTRACT OF THE DISCLOSURE

The present invention allows for processing classification and/or security filtering rules by using bitmaps as representations. In one instance, the packet header involved in the packet classification is divided into sections (fields) such as 16 bit portions. Once, this is performed, a data lookup table is built for each of the packet header fields. In particular, a bitmap is created representing which filter rules match a certain packet header field value. The created data lookup tables, typically one for each packet header field, are merged to form intermediate level data lookup tables. The intermediate level data lookup tables are continuously merged until one final data lookup table is formed. The result of the final data lookup table represents all the possible packets to be classified. Thus, each final data entry has a bitmap representing the filtering rules that matches this entry. The bitmap can be used to selectively provide a desired result of the classification. For instance, a first matching rule is represented by the first bit set in the bitmap; the best matching rule is determined by processing the bitmap and selecting the most appropriate rule; and a complete set of rules that match is represented by the full bits set in the bitmap.

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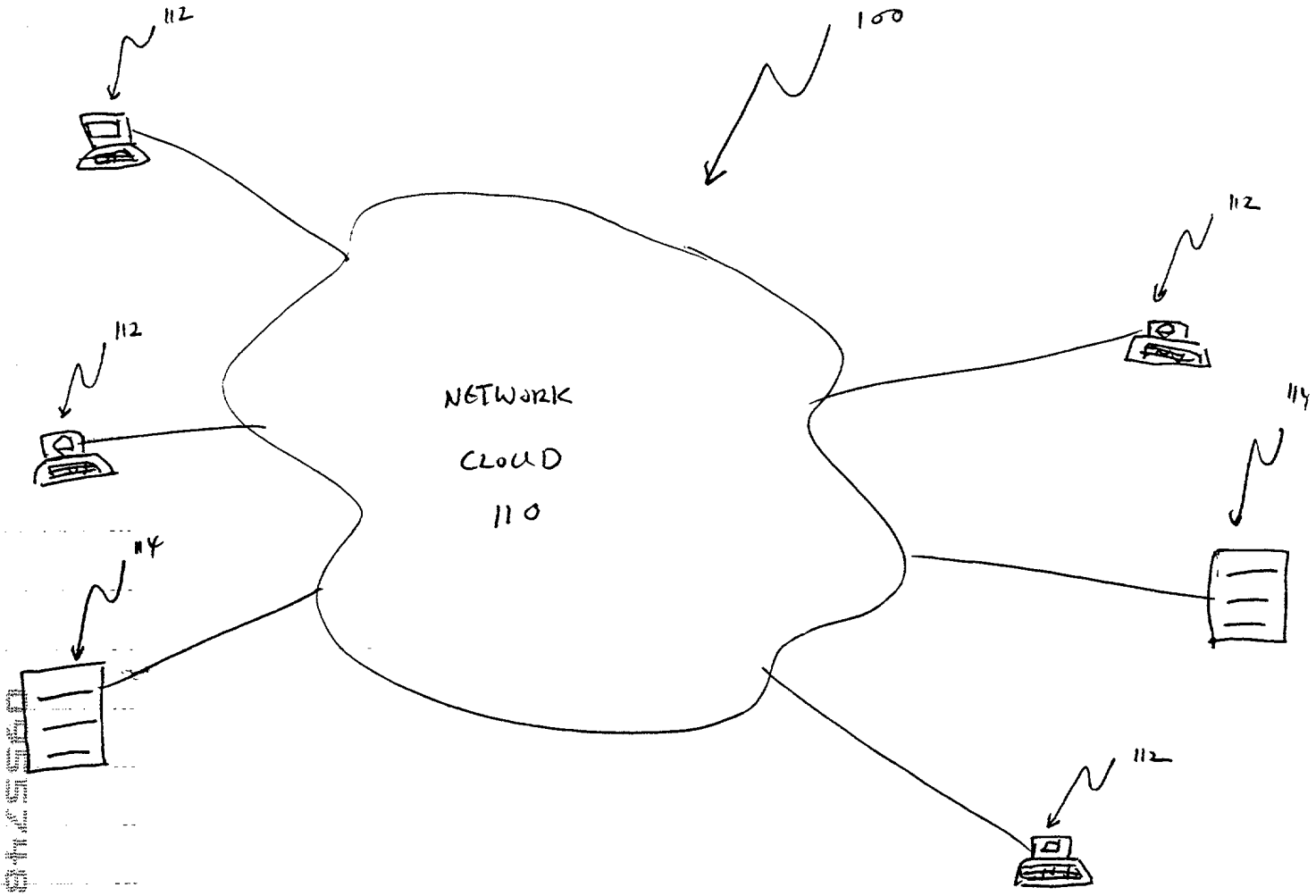
112
100
112
112
NETWORK
CLOUD
110
114
114
112

Fig. 1

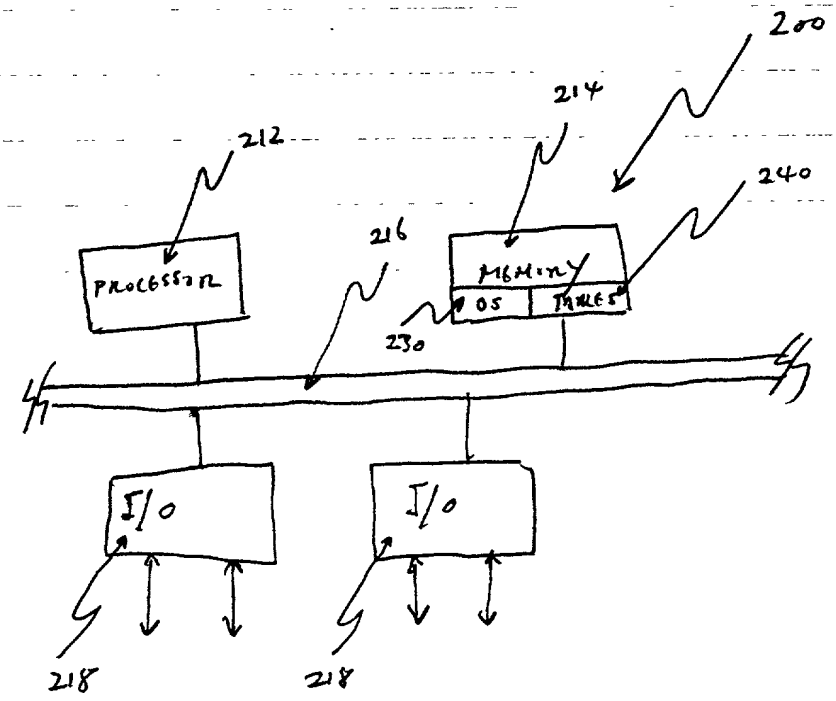
200
214
212
240
216
PROCESSOR
MEMORY
OS
TABLES
230
I/O
I/O
218
218

Fig 2

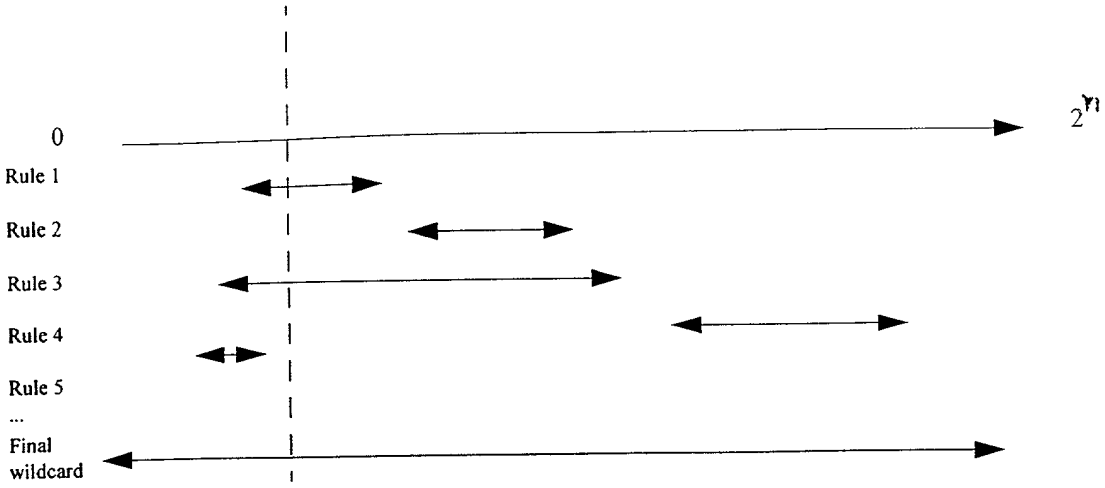

Fig. 3

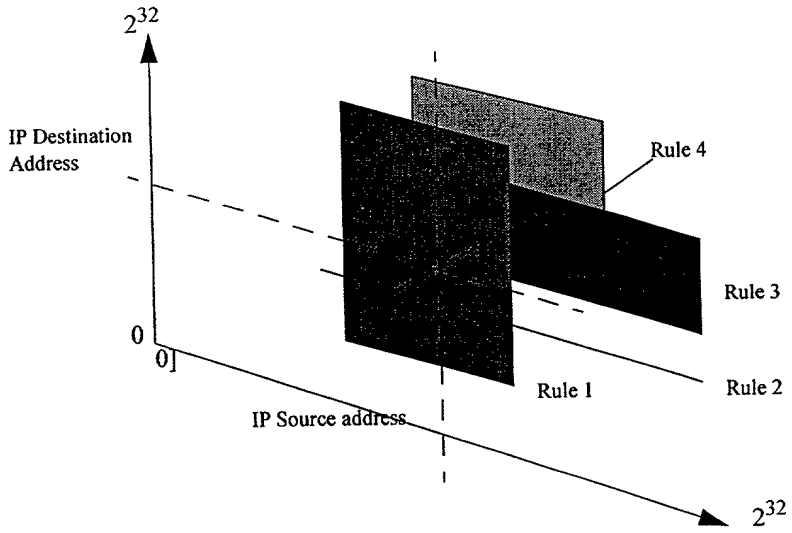
2^{32}
IP Destination Address
0
0
IP Source address
2^{32}
Rule 4
Rule 3
Rule 2
Rule 1

Fig. 4

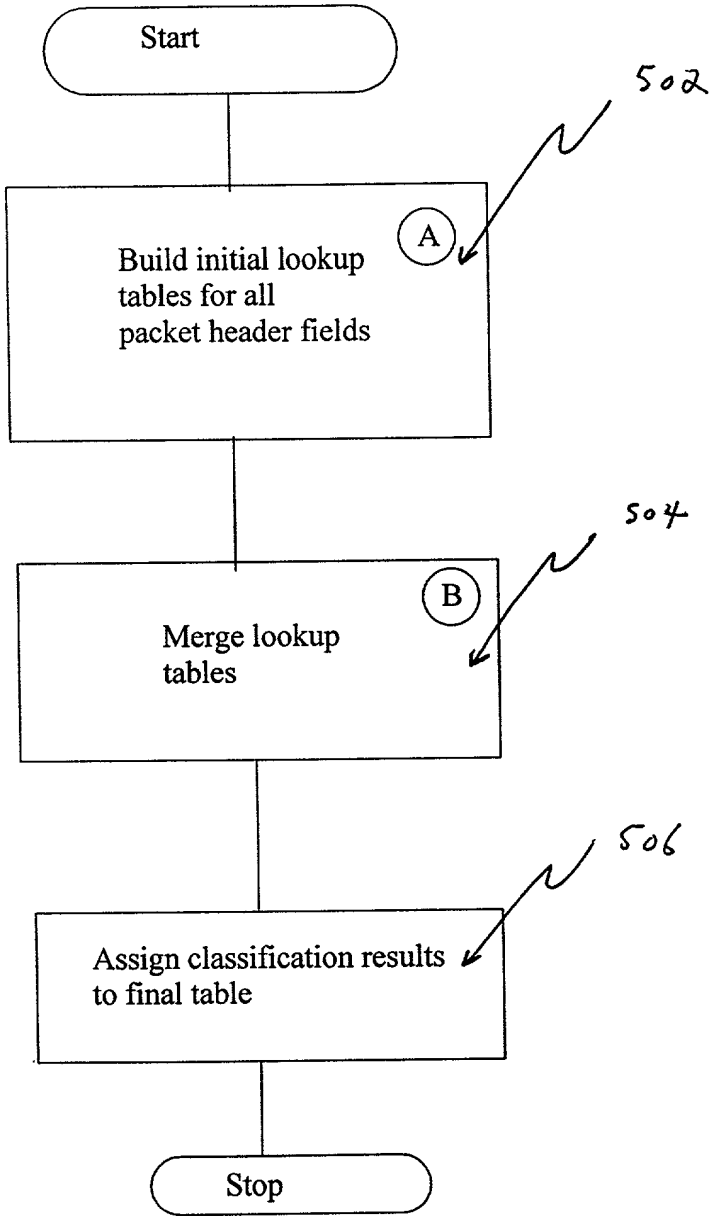
Start
502
A
Build initial lookup tables for all packet header fields
504
B
Merge lookup tables
506
Assign classification results to final table
Stop

Fig. 5

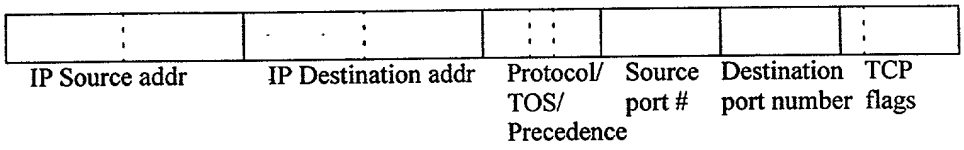
IP Source addr
IP Destination addr
Protocol/ TOS/ Precedence
Source port #
Destination port number
TCP flags

Fig. 6

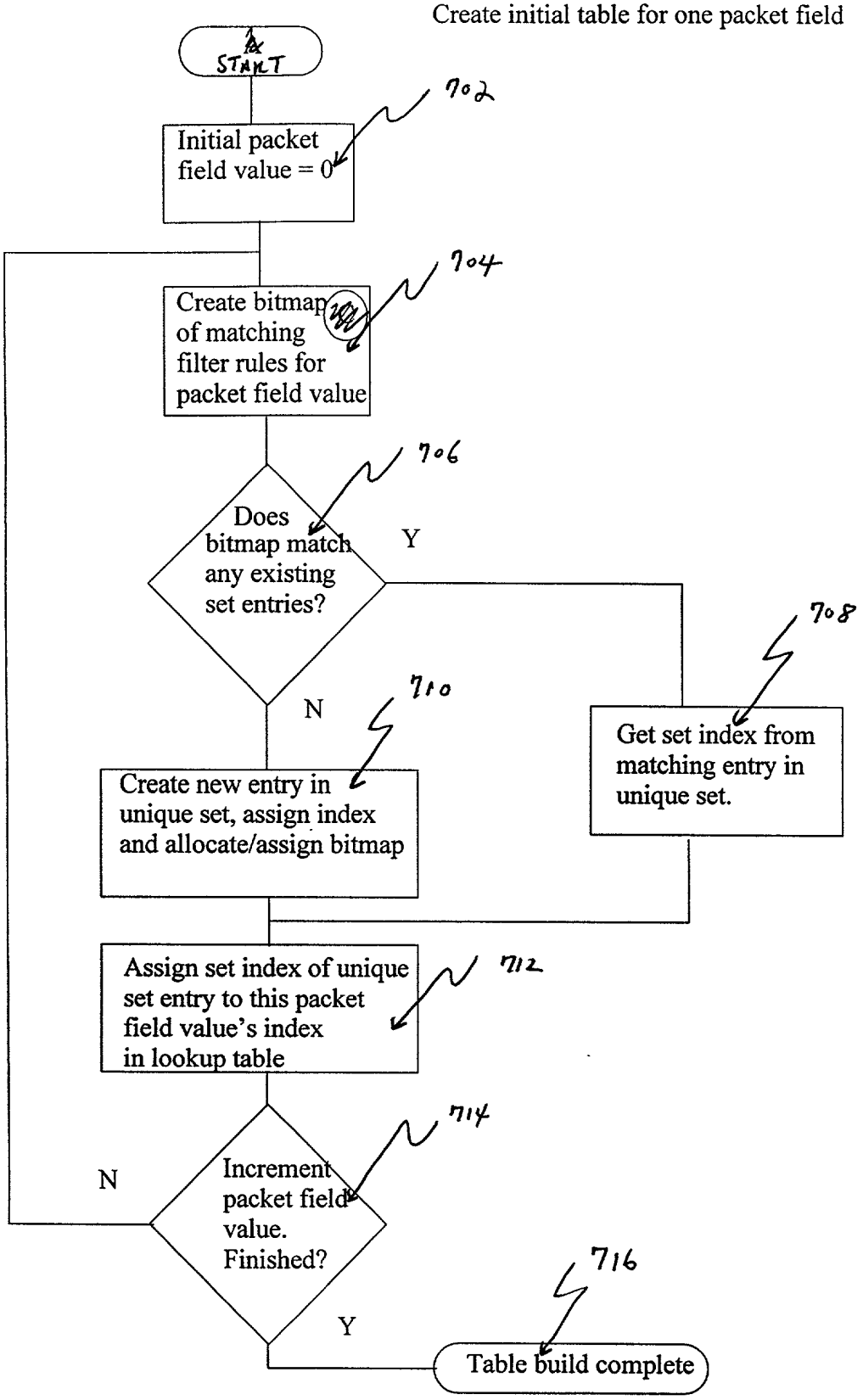
Create initial table for one packet field
START
Initial packet field value = 0
702
Create bitmap of matching filter rules for packet field value
704
Does bitmap match any existing set entries?
706
Y
N
Get set index from matching entry in unique set.
708
Create new entry in unique set, assign index and allocate/assign bitmap
710
Assign set index of unique set entry to this packet field value's index in lookup table
712
Increment packet field value. Finished?
714
N
Y
Table build complete
716

Fig. 7

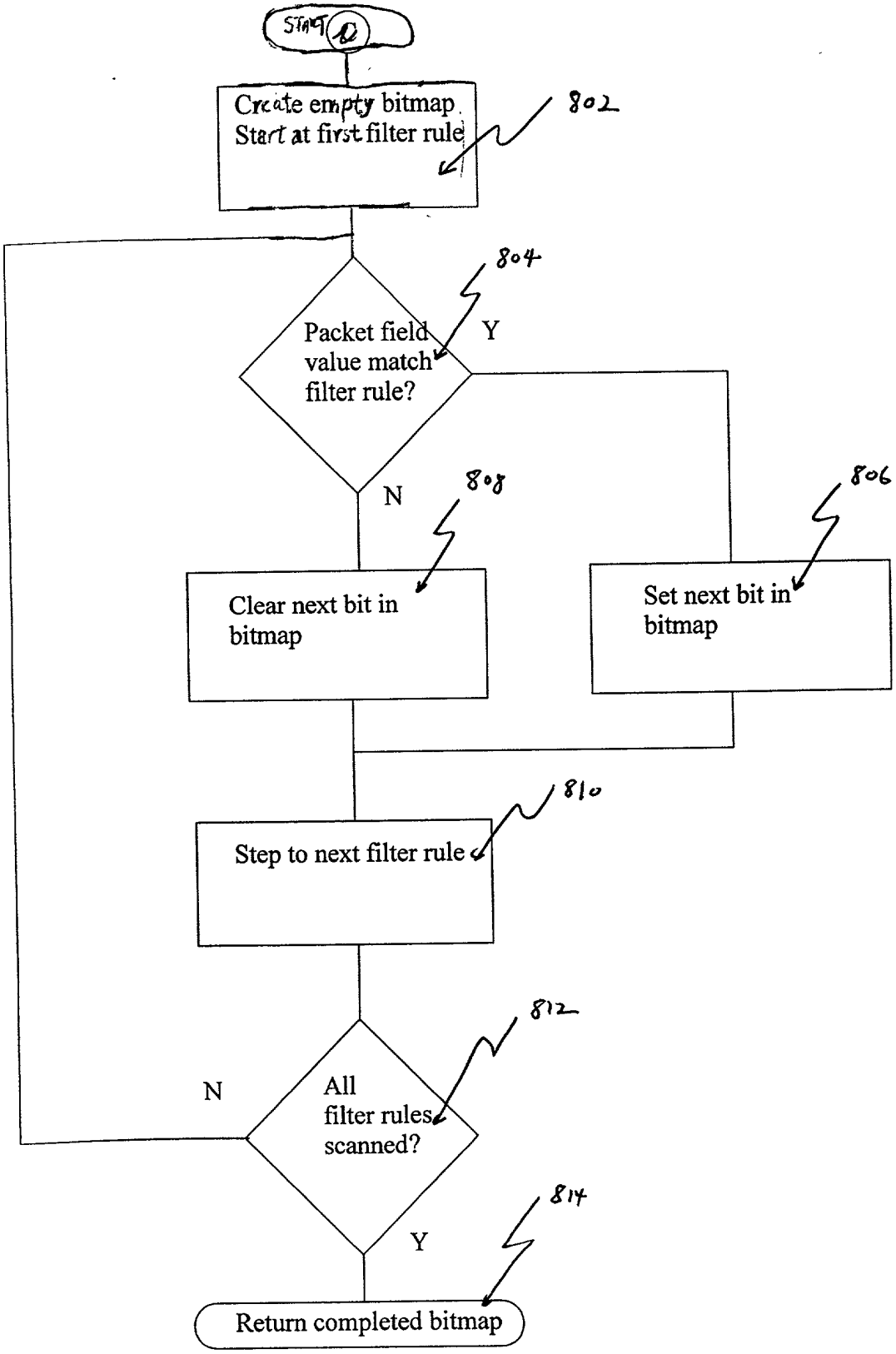
START
Create empty bitmap
Start at first filter rule
802
Packet field value match filter rule?
804
Y
N
808
Clear next bit in bitmap
806
Set next bit in bitmap
Step to next filter rule
810
All filter rules scanned?
812
N
Y
814
Return completed bitmap

Fig. 8

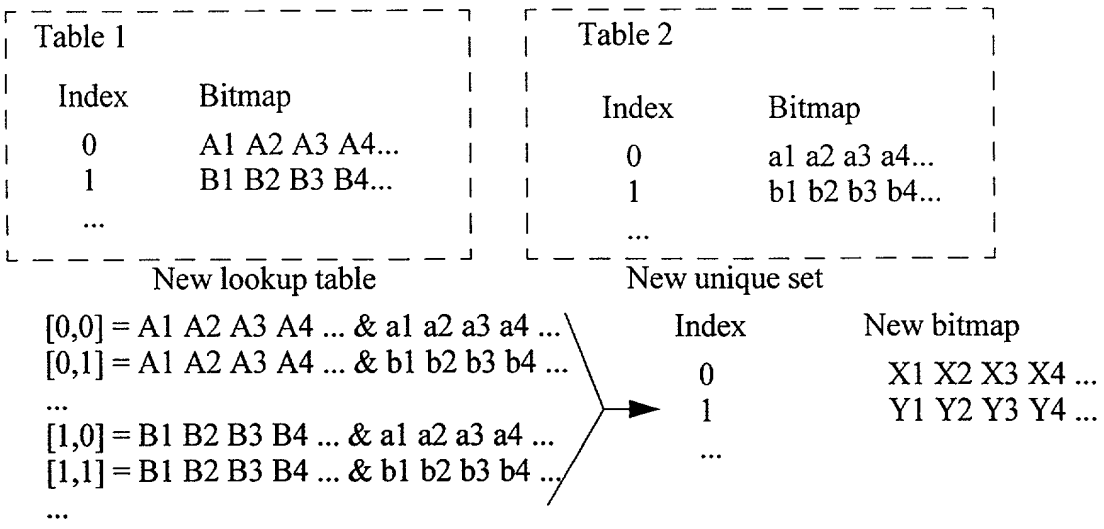
Table 1
Index
Bitmap
0
A1 A2 A3 A4...
1
B1 B2 B3 B4...
...
Table 2
Index
Bitmap
0
a1 a2 a3 a4...
1
b1 b2 b3 b4...
...
New lookup table
New unique set
[0,0] = A1 A2 A3 A4 ... & a1 a2 a3 a4 ...
[0,1] = A1 A2 A3 A4 ... & b1 b2 b3 b4 ...
...
[1,0] = B1 B2 B3 B4 ... & a1 a2 a3 a4 ...
[1,1] = B1 B2 B3 B4 ... & b1 b2 b3 b4 ...
...
Index
New bitmap
0
X1 X2 X3 X4 ...
1
Y1 Y2 Y3 Y4 ...
...

Fig. 9

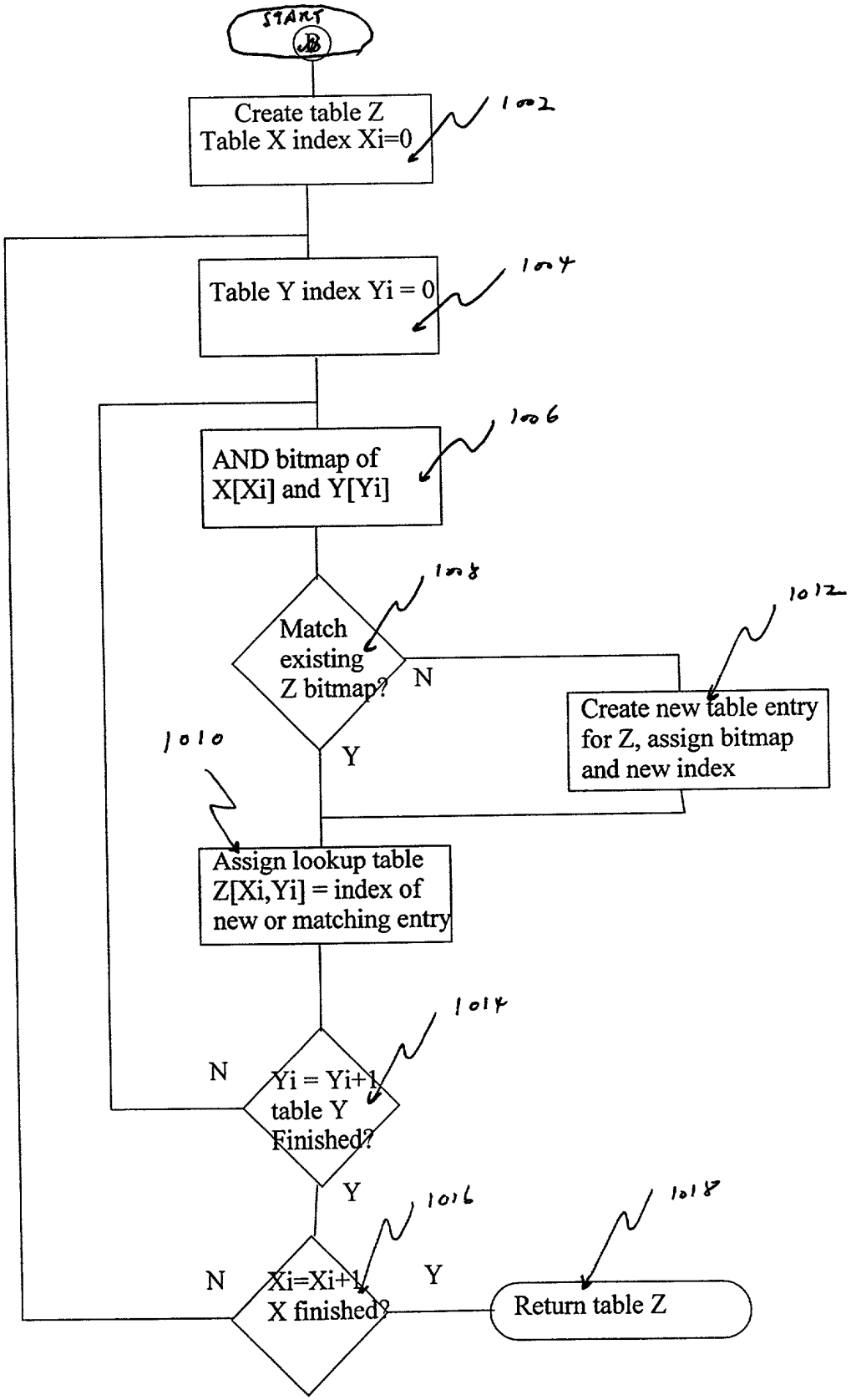
START
Create table Z
Table X index Xi=0
1002
Table Y index Yi = 0
1004
AND bitmap of
X[Xi] and Y[Yi]
1006
Match
existing
Z bitmap?
1008
N
Y
Create new table entry
for Z, assign bitmap
and new index
1012
1010
Assign lookup table
Z[Xi,Yi] = index of
new or matching entry
Yi = Yi+1
table Y
Finished?
1014
N
Y
Xi=Xi+1
X finished?
1016
N
Y
Return table Z
1018

Fig. 10

Upper 16 bits of IP source

Index	Bitmap
0	0 0 0 1 1
1	1 1 0 1 1
2	0 0 1 1 1

Lower 16 bits

Index	Bitmap
0	0 0 0 1 1
1	1 1 1 1 1

New lookup table

[0, 0]
[0,1]
[1,0]
[1,1]
[2,0]
[2,1]

New equivalence set

Index	Bitmap
0	0 0 0 1 1
1	1 1 0 1 1
2	0 0 1 1 1

Fig. 11

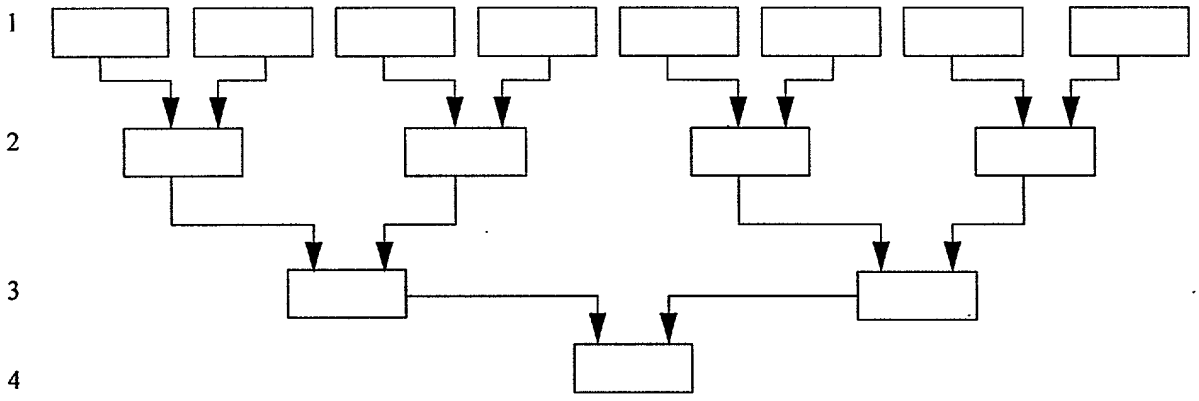

Fig. 12

DECLARATION FOR PATENT APPLICATION

As a below-named inventor, I hereby declare that:

My residence, post-office address, and citizenship are as stated below next to my name.

I believe I am the original, first, and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled A METHOD FOR HIGH SPEED PACKET CLASSIFICATION, the specification of which is attached hereto and identified by Cesari and McKenna File No. 112025-0180.

I hereby state that I have reviewed and understand the contents of the above-identified application specification, including the claims, as amended by any amendment specifically referred to herein.

I acknowledge the duty to disclose all information known to me that is material to patentability in accordance with Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code §119(a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate filed by me on the same subject matter having a filing date before that of the application on which priority is claimed: None.

I hereby claim the benefit under Title 35, United States Code §119(e) of the following U.S. provisional application: None.

I hereby claim the benefit under Title 35, United States Code §120, of the United States Application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United State Code, §112, I acknowledge the duty to disclose all information that is material to patentability in accordance with Title 37, Code of Federal Regulations, §1.56, and which became available to me between the filing date of the prior application and the national or PCT international filing date of this application: None.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Please direct all telephone calls to Yong S. Choi at (617) 951-3080. Please address all correspondence to Yong S. Choi.

Andrew McRae 20th April 2000

Andrew McRae Date

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Berowra NSW 2081
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Citizenship Australia

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